

**Workshop**  
**on**  
**" VLSI Design Tools, TCAD Simulation & Chip Manufacturing "**

Organized By

Department of Electronics and Communication Engineering

Faculty of Engineering and Technology

Gurukula Kangri (Deemed to be University), Haridwar, Uttarakhand

The Department of Electronics & Communication Engineering, Faculty of Engineering & Technology, Gurukul Kangri (Deemed to be University), Haridwar organized a two-day workshop on **"VLSI Design Tools, TCAD Simulation & Chip Manufacturing"** on **10–11 November 2025** at the New Seminar Hall. The workshop was conducted by **Mr. Amit Saini**, Director of Cadre Design System Pvt. Ltd., who provided detailed insights into modern VLSI design methodologies, CMOS fundamentals, TCAD-based device simulation, and semiconductor fabrication processes. A total of **65 students** from ECE and EE (2nd, 3rd, and 4th year) actively participated in the sessions. The workshop covered schematic design, simulation, layout creation, DRC/LVS verification, TCAD device modelling, and an in-depth overview of lithography, diffusion, metallization, and chip packaging techniques. Through hands-on practice and live demonstrations, students gained practical understanding of EDA and TCAD tools, improved their design skills, and developed stronger industry-oriented perspectives. The event significantly enhanced students' technical competencies and preparedness for careers in the semiconductor and chip design industry. In this program, Registrar, GKV, Prof. Vipul Sharma, Incharge Mr. Anuj Kumar Sharma, Mr. Prateek Agarwal, Mr. Atul Kumar Varshney and Mr. Sanjay Singh were present.

## Glimpse of Event:











**Attendance:**

Email Address	Contact no.	Student Name	Branch	In which year you are ?
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