CURRICULAM VITAE

PERSONAL DATA**:**

Name	:	Shiv Kumar Singh
Father's Name	:	R.P.Singh
Address	:	H.no 374, gali no.1, Lakshmi nagar, mohanpura, Roorkee,HARIDWAR.
Date of Birth	:	9 september 1979
Email ID	:	shiv.kumar@gkv.ac.in
Phone	:	09897108173

ACADEMIC PERFORMANCE**:**

Course : Ph.D. (pursuing)
Department : Electronics and communication Engineering
Institute name **: Gurukul kangri Vishwavidhyalya (Deemed to be university), Haridwar**

Course : Master of Technology   (With Teaching Assistance ship)
GATE %TILE : 82.84
YEAR of passing: FEB-2011
Department : Electronic & Communication Engineering
Institute name **: Dr. B. R. Ambedkar National Institute of Technology, Jalandhar** (An institute of National Importance)

Course : Bachelor of Technology (ECE)
Department : Electronics and communication Engineering
Institute name **: Gurukul kangri Vishwavidhyalya (Deemed to be university), Haridwar**
Year of passing **:** JULY-2008

Course : Diploma in Mechanical Engineering
Department : Mechanical Engineering
Institute name **: Govt.Polytechnic, sirsa, Haryana**
Year of passing **:** JULY-2001

ACHIVEMENTS:
1. "SUMMER FACULTY RESEARCH FELLOW PROGRAMME-2020" (online) under Continuing Education Programme from 05-JUNE-2020 to 10-JULY-2020, from IIT Delhi.
2. "Exploratory Applications of Control and Instrumentation" (online) from SEP 26-30, 2020, from Dr. B. R. Ambedkar National Institute of Technology, Jalandhar.
3. "Recent trends in Communication, Networking and Computing paradigms" (online), organized by department of ECE, university college of engineering & technology,

bikaner, technical university, bikaner from 7th to 18th sept. 2020

4. ''Orientation Program for NSS Program Officer'', Organized by The ministry of youth affairs and sports, Govt. of India, ETI-NSS Gurukul Kangri vishwavidalaya, from 16-DEC-2019 to 22-DEC-2019.

5. "Modelling and simulation of advanced semiconductor devices and VLSI circuits" Organized by GBPIET, Pauri-Garhwal, from JUNE 25-29, 2018.

6. "UGC-sponsored Refersher Course " from 02-DEC-2016 to 23-DEC-2016, at HRDC, Kumaun University, Nainital.

7. "MATLAB based Design Simulation" from MAR 07-11, 2016, at NITTR, Chandigarh

8. "Image Processing using VLSI Architectures" from JULY 06-10, 2015, at IIT Roorkee.

9. "UGC- sponsored Orientation Programme" from 27-OCT-2014 to 22-NOV-2014 at ASC, University of Rajasthan, Jaipur.

10. "ICT for Computing and Signal Processing" from JUNE 03-14 2013, at Dr. B. R. Ambedkar National Institute of Technology, Jalandhar.

COMPUTER SKILLS:

Languages	:	Programming languages -- C ,C++ Hardware description languages -- VHDL and Verilog
VLSI CAD Tool:		Xilinx tools for FPGA (ISE) Modelsim
DSP Tool	:	MATLAB: coding and simulink
Platforms	:	Windows and MSDOS; LINUX

PROJECT AND TRAINING:

Dissertation in M.Tech : COMPARATIVE INVESTIGATIONS ON BER IN CODED OFDM SYSTEMS WITH NAKAGAMI-M AND ALPHA-MU WIRELESS FADING DISTRIBUTIONS.

List of Publication :

- 1). S. K. Singh, N. Sood, and A. K. Sharma, "Performance Evaluation of LDPC and Turbocoded OFDM System in Nakagami-M Fading," *International Journal of Computer Theory and Engineering* vol. 4, no. 4, pp. 523-526, 2012.

- 2). Anuj Kumar Sharma and Shiv Kumar, "Resource Allocation in Cognitive Radio Network using Dirty Paper Coding," *IJCA Proceedings on National Conference on Future Aspects of Artificial intelligence in Industrial Automation 2012* NCFAAIIA(2):28-30, May 2012. Published by Foundation of Computer Science, New York, USA. Issn-973-93-80868-24-6

Project in B.Tech : Microcontroller 8051 based Digital   IC   Tester.
    An Integrated Circuit tester (IC tester) is used to test Integrated Circuits (ICs). We can easily test a digital IC using this kind of IC tester. this IC tester is affordable and user-friendly.

This IC tester is constructed, using 8051 microcontroller along with a keyboard and a display unit. It can test digital ICs having a maximum of 24 pins. Since it is programmable, any number of ICs can be tested .This IC tester can be used to test simple logic gates and also sequential and combinational ICs like flip-flops, counters, shift registers etc .

Training: – <u>Bharat Heavey Elctrical Limited , HARIDWAR</u>
Successfully completed Industrial Training of 4 Weeks

EXPERIENCE :
Teaching : 10 years

HOBBIES**:**

Cricket, Travelling, Photography & Playing Video games.

DECLARATION
      I hereby declare that all the information given above is correct to the best of my knowledge.

Place: ROORKEE   Shiv Kumar Singh